

CLAIMS:

1. A memory card comprising:
a memory;
a first connector electrically coupled to the memory and conforming to a first connector standard; and
a second connector electrically coupled to the memory and conforming to a second connector standard.
2. The memory card of claim 1, wherein the first and second connector standards comprise first and second device communication connector (DCC) standards.
3. The memory card of claim 2, wherein the first and second DCC standards comprise standards selected from a group consisting of: a Compact Flash standard, a Smart Media standard, a MultiMedia Card standard, a Secure Digital standard, a Memory Stick standard, and an xD standard.
4. The memory card of claim 1, wherein the first and second connector standards comprise first and second host computer connector (HCC) standards.
5. The memory card of claim 4, wherein the first and second HCC standards comprise standards selected from a group consisting of: a personal computer memory card international association (PCMCIA) standard, PC Card standard, a CardBus standard, a Universal Serial Bus (USB) standard, a Universal Serial Bus 2 (USB2) standard, an IEEE 1394 FireWire standard, a Small Computer System Interface (SCSI) standard, an Advance Technology Attachment (ATA) standard, a serial ATA standard, a Peripheral Component Interconnect (PCI) standard, and a conventional serial or parallel standard.

6. The memory card of claim 1, wherein the first connector standard comprises a host computer connector (HCC) standard and the second connector standard comprises a device communication connector (DCC) standard.

7. The memory card of claim 6, wherein:

the HCC comprises a standard selected from a group consisting of: a personal computer memory card international association (PCMCIA) standard, a PC Card standard, a CardBus standard, a Universal Serial Bus (USB) standard, a Universal Serial Bus 2 (USB2) standard, an IEEE 1394 FireWire standard, a Small Computer System Interface (SCSI) standard, an Advance Technology Attachment (ATA) standard, a serial ATA standard, a Peripheral Component Interconnect (PCI) standard, and a conventional serial or parallel standard; and

the DCC comprises a standard selected from a group consisting of: a Compact Flash standard, a Smart Media standard, a MultiMedia Card standard, a Secure Digital standard, a Memory Stick standard, and an xD standard.

8. The memory card of claim 1, wherein the first connector is disposed on a different side of the memory card than the second connector.

9. The memory card of claim 8, wherein the first connector is disposed on an opposite side of the memory card relative to the second connector.

10. The memory card of claim 1, wherein at least one of the first connector and the second connector comprises a retractable connector.

11. The memory card of claim 10, further comprising:

a housing defining a slot for the retractable connector; and

a first electrical contact on the retractable connector and a second electrical contact within the slot, wherein the first electrical contact couples to the second electrical contact when the retractable connector is extended from the slot.

12. The memory card of claim 10, wherein the first connector is disposed on the same side of the memory card as the second connector.
13. The memory card of claim 12, wherein a set of electrical contact elements of the first connector comprise a subset of a set of electrical contact elements of the second connector.
14. The memory card of claim 1, further comprising:
 - a memory controller electrically coupled to the memory;
 - a first connector controller electrically coupled to the first connector and the memory controller, the first connector controller conforming to the first connector standard; and
 - a second connector controller electrically coupled to the second connector and the memory controller, the second connector controller conforming to the second connector standard, wherein the first connector is electrically coupled to the memory through the first connector controller and the memory controller, and the second connector is electrically coupled to the memory through the second connector controller and the memory controller.
15. The memory card of claim 1, further comprising:
 - a first controller electrically coupled to the memory and the first connector, the first controller controlling the memory and output via the first connector; and
 - a second connector controller electrically coupled to the second connector and the first controller, the second connector controller controlling output via the second connector and conforming to the second connector standard, wherein the first connector is electrically coupled to the memory through the first controller, and the second connector is electrically coupled to the memory through the second connector controller and the first controller.
16. The memory card of claim 15, wherein the first controller comprises a memory controller integrated with a first connector controller conforming to the first connector standard.

17. The memory card of claim 1, further comprising a controller that controls the memory and output via the first connector and the second connector, wherein the first and second connectors are electrically coupled to the memory through the controller.

18. The memory card of claim 17, wherein the controller comprises a memory controller integrated with a first connector controller conforming to the first connector standard and a second connector controller conforming to the second connector standard.

19. The memory card of claim 1, further comprising a third connector electrically coupled to the memory and conforming to a third connector standard.

20. The memory card of claim 19, further comprising a fourth connector electrically coupled to the memory and conforming to a fourth connector standard.

21. A method comprising:

delivering power to a memory card comprising a memory, a first connector electrically coupled to the memory and conforming to a first connector standard, and a second connector electrically coupled to the memory and conforming to a second connector standard;

detecting whether power is delivered via the first connector or the second connector;
and

enabling a controller to facilitate access to the memory based on whether power is delivered via the first connector or the second connector.

22. The method of claim 21, further comprising:

reading data stored in the memory via the powered connector and the enabled controller; and

writing data to the memory via the powered connector and the enabled controller.

23. A system comprising:

a first device including a first electrical contact for receiving a connector that conforms to a first connector standard;

a second device including a second electrical contact for receiving a connector that conforms to a second connector standard; and

a memory card including:

a memory,

a first connector conforming to the first connector standard such that the first connector can be received by the first electrical contact of the first device, and

a second connector conforming to the second connector standard such that the second connector can be received by the second electrical contact of the second device.

24. The system of claim 23, wherein the first and second connector standards comprise first and second device communication connector (DCC) standards selected from a group consisting of: a Compact Flash standard, a Smart Media standard, a MultiMedia Card standard, a Secure Digital standard, a Memory Stick standard, and an xD standard.

25. The system of claim 23, wherein the first connector standard comprises a device communication connector (DCC) standard selected from a group consisting of: a Compact Flash standard, a Smart Media standard, a MultiMedia Card standard, a Secure Digital standard, a Memory Stick standard, and an xD standard; and

the second connector standard comprises a host computer communication connector (HCC) standard selected from a group consisting of: a personal computer memory card international association (PCMCIA) standard, a PC Card standard, a CardBus standard, a Universal Serial Bus (USB) standard, a Universal Serial Bus 2 (USB2) standard, an IEEE 1394 FireWire standard, a Small Computer System Interface (SCSI) standard, an Advance Technology Attachment (ATA) standard, a serial ATA standard, a Peripheral Component Interconnect (PCI) standard, and a conventional serial or parallel standard.

26. The system of claim 23, wherein the first and second connector standards comprise first and second host computer connector (HCC) standards selected from a group consisting

of: a personal computer memory card international association (PCMCIA) standard, a PC Card standard, a CardBus standard, a Universal Serial Bus (USB) standard, a Universal Serial Bus 2 (USB2) standard, an IEEE 1394 FireWire standard, a Small Computer System Interface (SCSI) standard, an Advance Technology Attachment (ATA) standard, a serial ATA standard, a Peripheral Component Interconnect (PCI) standard, and a conventional serial or parallel standard